NY4 Series System Base Timer Application Notice

Content: NY4 series provides BT register (system base timer at memory address M[0x8]) without setting its initial status and letting it be unknown. Therefore, users must write a certain value to configure BT register before reading the counter value of timer from BT register.

Reason: The initial status of BT register of NY4 is unknown.

Example:

```
BT      EQU   0x8
        .
        .
        .
        MVLA    0x0             ;0x0: BT = 0.125 ms; 0x1: BT = 0.25 ms; 0x2: BT = 1 ms; 0x3: BT = 4 ms
        MVAM    BT              ;Set base timer as 0.125 ms
        .
        .
        .
        MVMA    AUD
        ANDL    B'0100'
        CPAL    0x4             ; AUD[2] = 1 means BT time out
        JMP     BTFLG_LOW
        JMP     BTFLG_HIGH
        .
        .
        .
BTFLG_LOW:
        .
        .
        .
BTFLG_HIGH:
```